

m-701 US 1/5

# Vertice Data Feeding Circuit 100

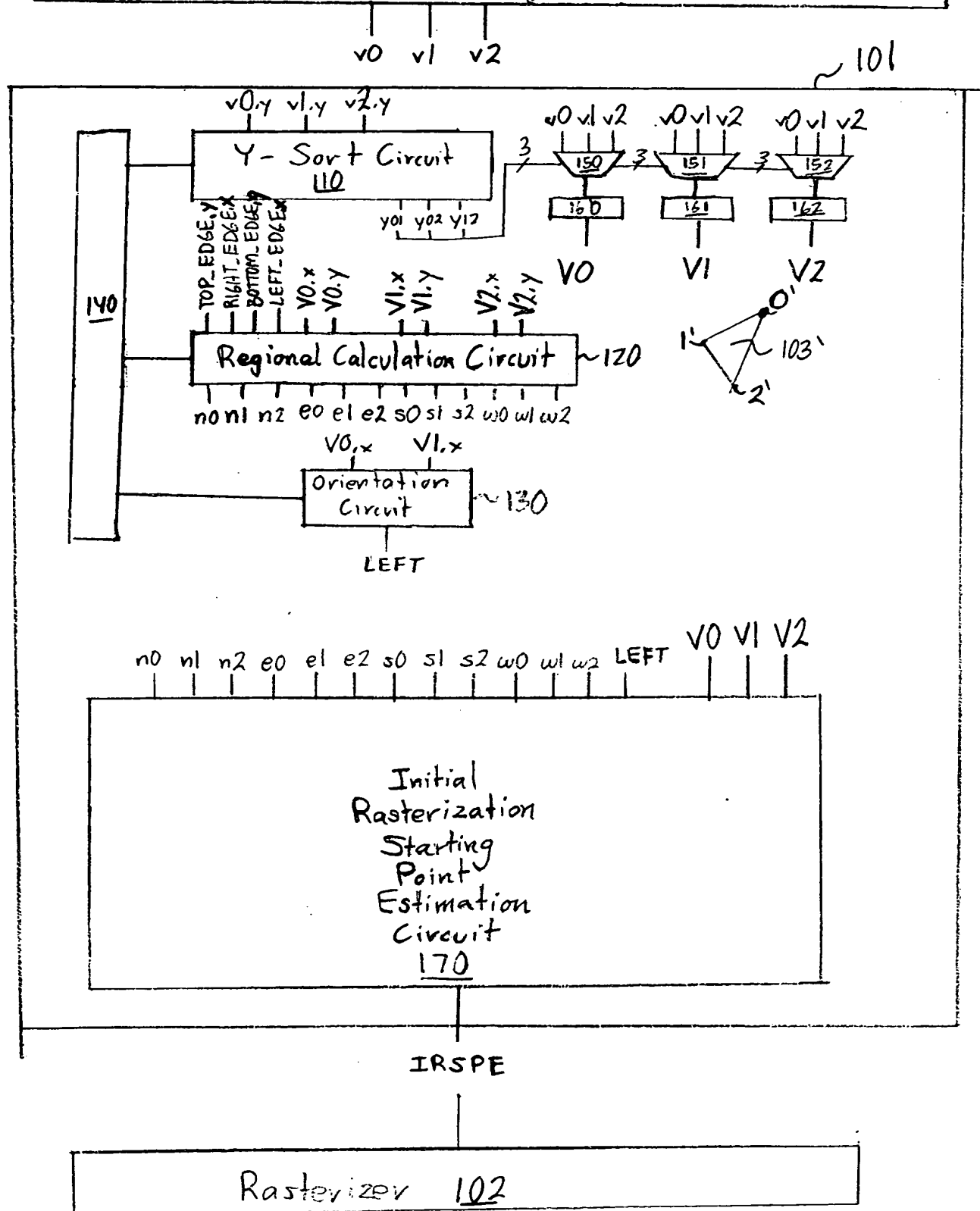


FIG. 1

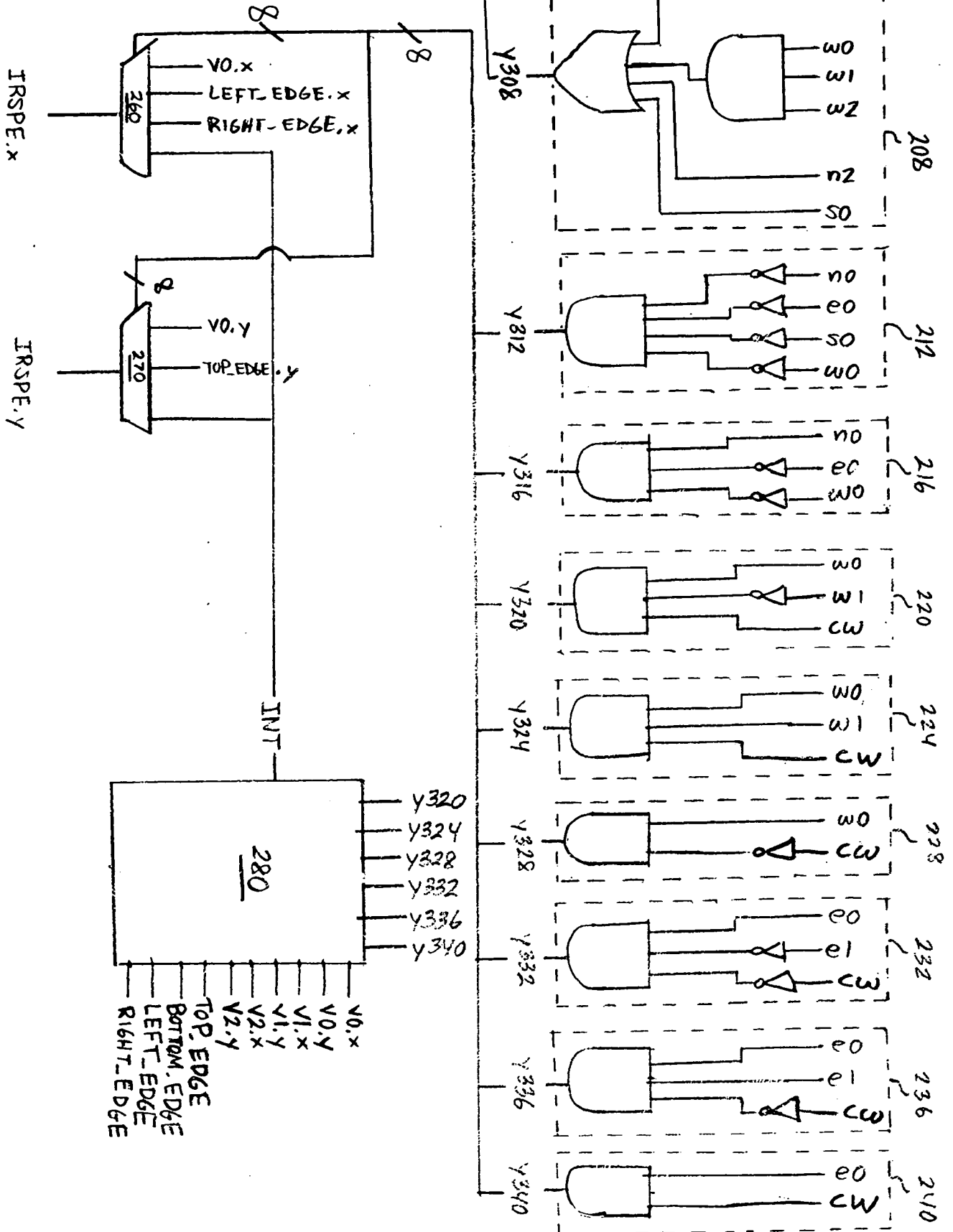
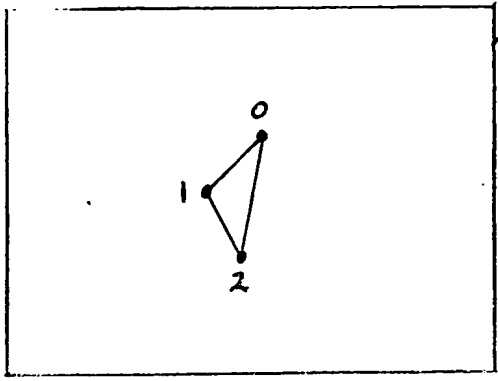
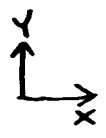
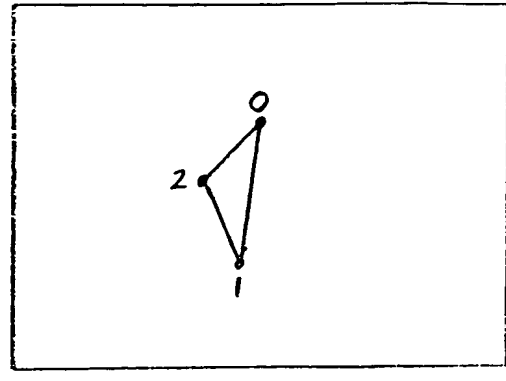
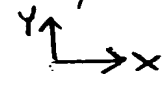


FIG. 2



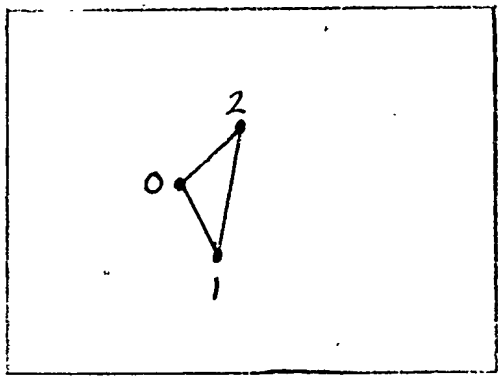
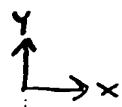
~300

FIG. 3A



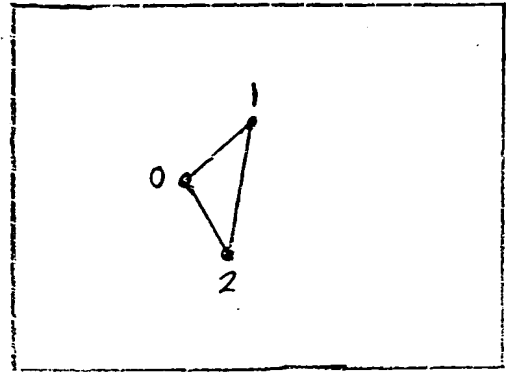
~300

FIG. 3B



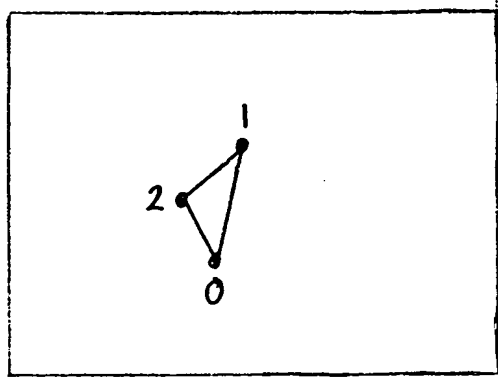
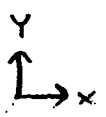
~300

FIG. 3C



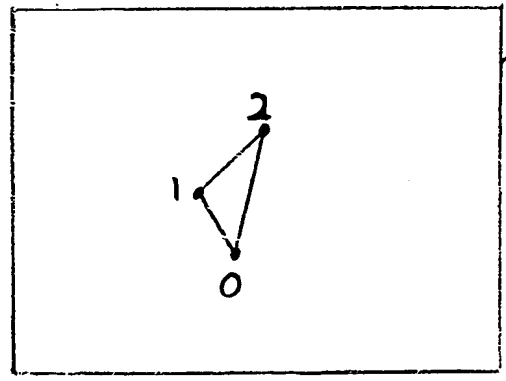
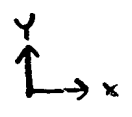
~300

FIG. 3D



~300

FIG. 3E



~300

FIG. 3F

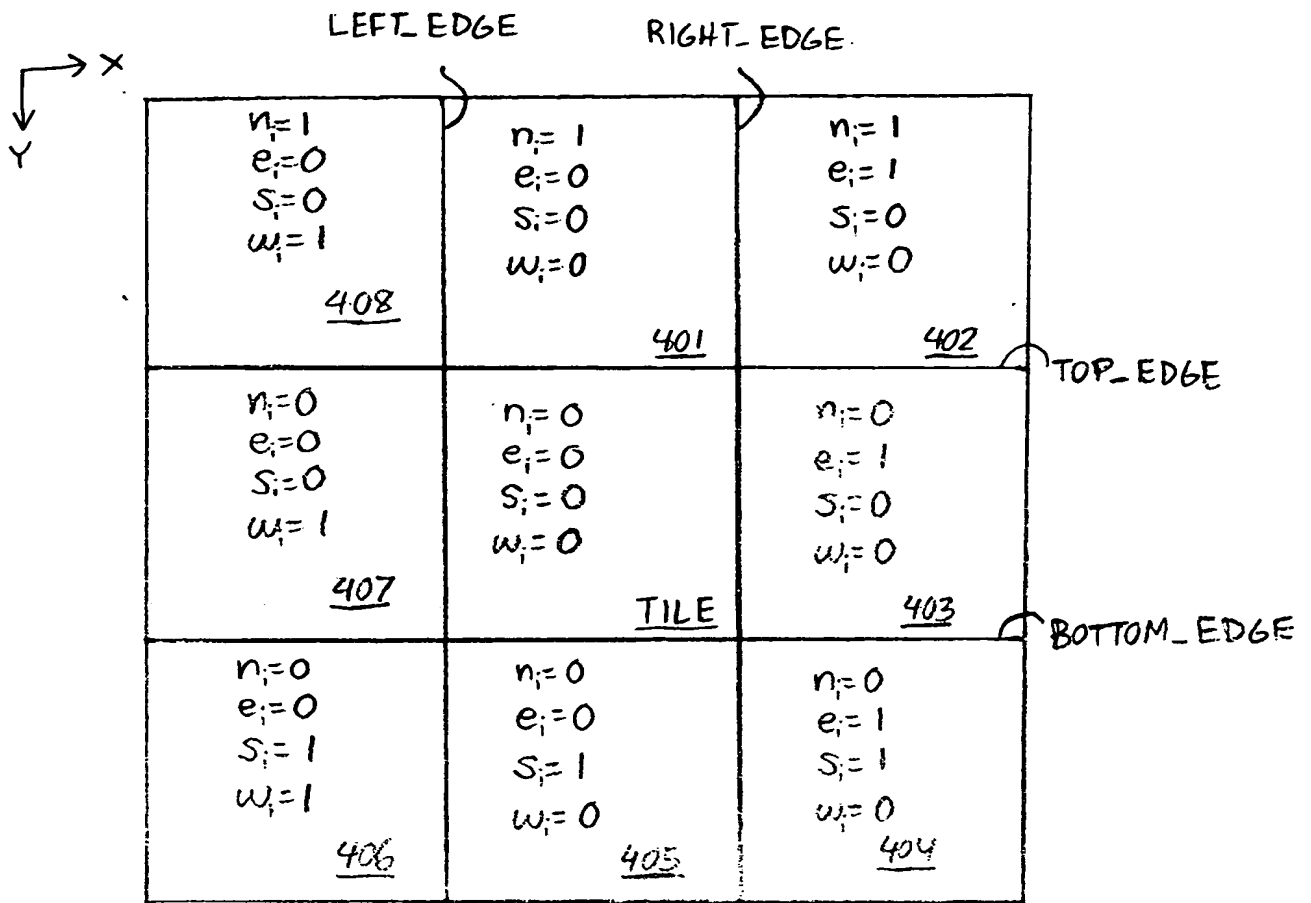


FIG. 4

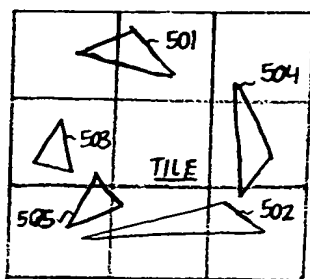


FIG. 5

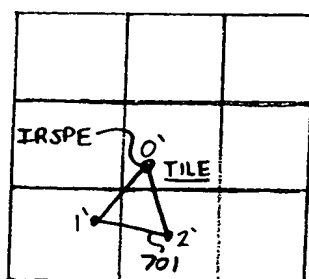


FIG. 6

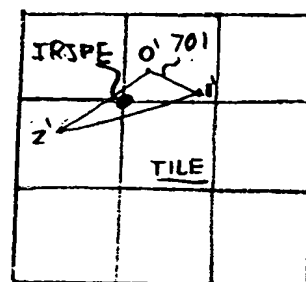


FIG. 7

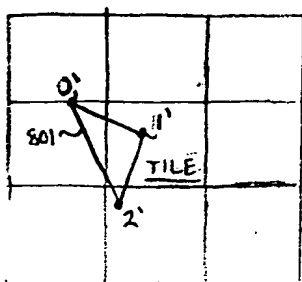


FIG. 8

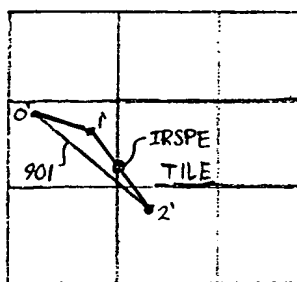


FIG. 9

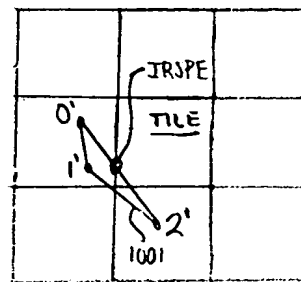


FIG. 10

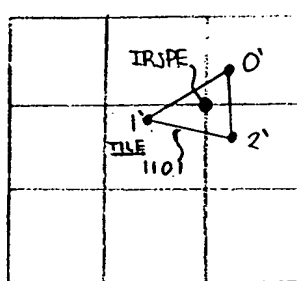


FIG. 11

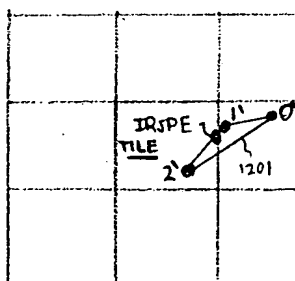


FIG. 12

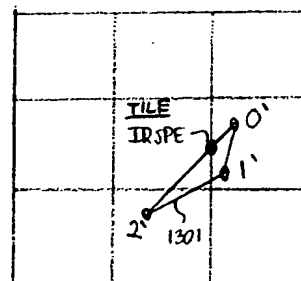


FIG. 13